

WHAT IS CLAIMED IS:

1. A liquid crystal display comprising:
 - a first substrate;
 - a gate line formed on the first substrate;
 - 5 a data line formed on the first substrate and intersecting the gate line;
 - first and second pixel electrodes located opposite the data line and having a plurality of first cutouts;
 - a thin film transistor connected to the gate line, the data line, and the first pixel electrode;
 - 10 a second substrate facing the second substrate; and
 - a common electrode formed on the second substrate and having a plurality of second and third cutouts defining a plurality of domains along with the first cutouts and facing the first and the second pixel electrodes, respectively,
 - wherein the second and the third cutouts have portions overlapping the data
 - 15 line and the overlapping portions of the second and the third cutouts are alternately arranged along the data line.
2. The liquid crystal display of claim 1, wherein the second and the third cutouts are alternately arranged with the first cutouts of the first and the second pixel electrodes.
- 20 3. The liquid crystal display of claim 2, wherein the domains have two long edges extending substantially parallel to each other and make an angle of about 45 degrees with the gate line.
4. The liquid crystal display of claim 3, wherein the second cutouts include upper cutouts facing upper half of the first pixel electrode and lower cutouts
- 25 facing lower half of the first pixel electrode, and at least one of the number of the upper cutouts and the number of the lower cutouts is odd.
5. A liquid crystal display comprising:
 - a first substrate;
 - a gate line formed on the first substrate and extending substantially in a first
 - 30 direction;
 - a gate insulating layer formed on the gate line;
 - a semiconductor layer formed on the gate insulating layer;

first and second ohmic contacts formed on the semiconductor layer;
a data line formed on the first substrate, extending in a second direction, and
including a source electrode disposed on the first ohmic contact at least in part;
a drain electrode formed on the second ohmic contact at least in part;
5 a passivation layer formed on the data line and the drain electrode and having
a contact hole exposing the drain electrode;

first and second pixel electrodes formed on the passivation layer and having a
plurality of first cutouts, the first pixel electrode connected to the drain electrode
through the contact hole;

10 a second substrate facing the first substrate;
a plurality of color filters formed on the second substrate; and
a common electrode formed on the color filters and having a plurality of
second and third cutouts defining a plurality of domains along with the first cutouts
and facing the first and the second pixel electrodes, respectively,

15 wherein the second and the third cutouts have portions overlapping the data
line, each of the second and the third cutouts include upper cutouts facing upper half
of the first or the second pixel electrodes and lower cutouts facing lower half of the first
or the second pixel electrodes, and the number of the upper cutouts and the number of
the lower cutouts are odd.

20 6. The liquid crystal display of claim 5, wherein the overlapping
portions of the second and the third cutouts are alternately arranged along the data
line.

7. The liquid crystal display of claim 5, further comprising a storage
electrode line located on the same plane as the gate line and overlapping the first and
25 the second pixel electrodes.